First Semester M. E. (Digital Etx.) Full Time Examination

EMBEDDED SYSTEM DESIGN

P. Pages: 2	
Time : Three Hours]	
Note: (1) Day	[Max. Marks: 80
Note: (1) Due credit will be given to neatness and adequate diri (2) Assume suitable data wherever necessary. (3) Illustrate your answer wherever necessary with the help	nensions. of near sketches
1. (a) Describe ARM register set, also specify the flags in Al	RM-7 processer.
(b) Draw and explain embedded system with SOC.	7
on plant embedded system with SOC.	6
OR	J
2. (a) Explain the 1 v	
challenges in embedded system.	_
(b) Draw the interfacing of 64 K v 4 DANG	6
(b) Draw the interfacing of 64 K x 4 RAM and 32 K x 4 RO 7 processor. Show all relevant signals.	M with ARM-
Jagania,	7
3. (a) How the interrupts are handled in C? Give example. What latency? What is its effect on dead line?	is an interrupt
(b) Describe the techniques to write	7
(b) Describe the techniques to write an efficient code in C.	6.
OR	·
4. (a) Describe the feature of	
the realtire of start-up code provided in Casesses	oiler. 6
(b) What are the various data types supported in C for ARM Explain with example.	
	7
 Draw the interfacing of stepper motor and 16 x Z LCD with microcontroller? Write the C program to rotate motor clockwise wit of 30 for total 100 revolution and accordingly display revolution LCD unit. AQ-2793 	LPC 2148 h step angle number on

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OR

5. Draw the interfacing of 4 x 4 keyboard matrix and 16 x 2 LCD with L 2148 microcontroller. Write the C program to read key character from keybo	PC ar,d 14
and display on LCD.	
model with example.	7
7. (a) Explain in detail preemptive/non-preemptive model with example.	6
(b) Explain task control block.	
OR	
Mutuy and Semaphore.	7
8. (a) Differentiate between Mutux and Semaphore.	6
(b) Define threads, task, process and jobs.	
	7
9. (a) Explain RM algorithm.	6
(b) Explain Micros OS-II.	
OR	
c and assignment	6
10. (a) Discuss aspects of task assignment.	7
(b) Explain Earliest Deadline First algorithm with example.	
11. (a) Explain the concept of validation and debugging of embedded s	ystem. 7
(b) Discuss the ways of computing appropriate stack sites for task	. 7
OR	
12. (a) What is hardware software co-design? Explain the fundamental in hardware software co-design.	ssues in
(b) Why is it difficult to estimate the size of product at the begin product?	nning of 6
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